

**PATENT APPLICATION**

Attorney Docket: 54364

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Gupta, et al.
Serial No.:	09/847,667
Filed:	5/1/2001
For:	Method for Bonding Wafers to Produce Stacked Integrated Circuits
Group Art Unit:	2827
	Examiner: James Mitchell

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**TRANSMITTAL OF RESPONSE AND PETITION FOR EXTENSION OF TIME**

In response to the office action dated 1/17/03, Applicant submits the accompanying Response and a Petition for a 3 months extension of time in the above-identified patent application.

The Commissioner is hereby authorized to charge any fees which may be required in this application under 37 C.F.R. Section 1.16-1.17 during its entire pendency, or credit any overpayment, to Deposit Account No. 23-0424. This sheet is filed in duplicate.

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Date: July 7, 2003

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